Docket No.: 057454-0060   PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Response Under 37 CFR 1.116 - Expedited Procedure
                                           : Customer Number: 20277

Isao MINEMATSU   : Confirmation Number: 3710

Application No.: 09/819,990   : Group Art Unit: 2183

Filed: March 29, 2001   : Examiner: Daniel H. Pan

For: MICROPROCESSOR EXECUTING DATA TRANSFER BETWEEN MEMORY AND REGISTER AND DATA TRANSFER BETWEEN REGISTERS IN RESPONSE TO SINGLE PUSH/POP INSTRUCTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.
☒   No additional fee is required.
☐   Applicant is entitled to small entity status under 37 CFR 1.27
☐   Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	10	20	0	$50.00 =	$0.00
Independent Claims	1	3	0	$200.00 =	$0.00
	Multiple claims newly presented				$0.00
	Fee for extension of time				$0.00
					$0.00
	Total of Above Calculations				$0.00

☐   Please charge my Deposit Account No. 500417 in the amount of $0.00. An additional copy of this transmittal sheet is submitted herewith.

☒   The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

Alexander V. Yampolsky
Registration No. 36,324

600 13th Street, N.W.   **Please recognize our Customer No. 20277 as our**
Washington, DC 20005-3096   **correspondence address.**
Phone: 202.756.8000 SAB/AVY/dlb
Facsimile: 202.756.8087
Date: March 21, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Isao MINEMATSU	RESPONSE UNDER 37 CFR 1.116 <u>EXPEDITED PROCEDURE</u>
Application No.: 09/819,990	Customer No.: 20277
Filed: March 29, 2001	Confirmation No.: 3710
Group Art Unit: 2183	Examiner: Daniel H. Pan

Title: MICROPROCESSOR EXECUTING DATA TRANSFER BETWEEN MEMORY AND REGISTER AND DATA TRANSFER BETWEEN REGISTERS IN RESPONSE TO SINGLE PUSH/POP INSTRUCTION

AMENDMENT UNDER 37 CFR 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed December 21, 2004, please amend the above-identified application as follows: